H·O·D

23/4/14

M 6021

Reg. No. : .....

Name : .....

VI Semester B.Sc. Degree (CCSS – Reg./Supple./Improv.) Examination, May 2014
Core Course in Computer Science
6B15CSC : COMPUTER ORGANISATION

Time : 3 Hours Max. Weightage : 21

SECTION – A

Answer **all** questions. Weightage for a bunch of **four** questions is **1**.

1. The type of the addressing mode in which the effective address is equal to the address part of the instruction is
   a) Direct Address Mode
   b) Indirect Address Mode
   c) Register Indirect
   d) Immediate

2. The register that hold the address of the stack is
   a) Stack Address Pointer
   b) Stack Pointer
   c) Stack Register Pointer
   d) Register Pointer

3. The data register is sometimes called
   a) Address Register
   b) Pipeline Register
   c) Buffer Address Register
   d) Memory Address

4. The transfer of information from a memory word to outside environment is
   a) Memory Write
   b) Memory Read Write
   c) Memory Read
   d) None of these

5. The third state of a three state bus buffer is
   a) Binary 0
   b) Binary 1
   c) High impedance
   d) None of these

P.T.O.

6. Which condition can be detected by observing the carry into the sign bit position and the carry out of sign bit position

   a) Overflow b) Underflow

   c) Both d) None of these

7. If the most significance digit of mantissa of floating point number is non zero. Then the number is said to be

   a) Gray code b) Normalized

   c) Fraction d) Exponent

8. The register that keeps track of address of the instruction to be executed is called

   a) Accumulation b) MAR

   c) MBR d) Program counter **(2×1=2)**

SECTION – B

Answer **any 5** questions. Weightage **1 each**.

9. Explain the Relative Address Mode.

10. What are the difference between the multiprocessors and multi computers ?

11. Explain floating point representaties.

12. What is a register transfer ?

13. What is an effective address ?

14. Write three memory reference instructions.

15. What is interrupt cycle ?

16. What are major phases of operation of control unit when go through an instruction cycle ? **(5×1=5)**

SECTION – C

Answer **5** questions. Weightage **2 each**.

17. What is a RISC ?
18. Explain Base Register Address Mode.
19. Explain different Auxiliary Memory Types.
20. Explain memory stack.
21. Explain 2's complement addition and 2's complement subtraction.
22. Explain address sequencing.
23. What is the general register organisation ?
24. Explain the Register Indirect mode. **(5×2=10)**

SECTION – D

Answer **any one** question. Weightage **4**.

25. Explain the Fixed Point and Floating Point Representation.
26. Explain the Direct Memory Access in detail. **(1×4=4)**