



Reg. No. : .....

Name : .....

K18U 0

VI Semester B.Sc. Degree (CCSS-Supple.) Examination, May 2018  
CORE COURSE IN COMPUTER SCIENCE  
6B15 CSC : Computer Organization (2012 – 13 Admns.)

Time : 3 Hours

Max. Weightage : 21

SECTION - A

- Answer **all** questions. Weightage for a bunch of **four** questions is **1**.
- Which register stores the address of the next instruction to be executed ?
    - Instruction register
    - Accumulator
    - Program counter
    - Memory address register
  - 1-way set associative mapping reduces to
    - direct mapping
    - indirect mapping
    - associative mapping
    - none of the above
  - Which of the following is the fastest memory ?
    - Primary memory
    - Secondary memory
    - Cache memory
    - Solid state device
  - Cache memory works on the principle of
    - locality of data
    - locality of memory
    - locality of reference
    - locality of reference and memory
  - Which of the following is a volatile memory ?
    - RAM
    - ROM
    - Hard disc
    - Pen drive
  - Address mapping with less number of TAG bits is
    - direct mapping
    - associative mapping
    - set-associative mapping
    - none of the above
  - The zero flag is set when the
    - input becomes zero
    - result of most recent operation becomes zero
    - result of most recent operation becomes non-zero
    - none of the above
  - Addressing mode in which the operand itself is specified in the instruction known as
    - implied addressing mode
    - indirect addressing mode
    - direct addressing mode
    - immediate addressing mode



## SECTION – B

Answer **any 5** questions. Weightage **1** for **each**.

9. What is microprogram ?
10. What do you mean by instruction ?
11. What is the need of addressing modes ?
12. Differentiate static RAM and dynamic RAM.
13. What do you mean by I/O processor ?
14. What is auxiliary memory ?
15. With the help of a block diagram explain the control unit of a basic computer.
16. What do you mean by strobe pulse ? What are its limitations ? (5×1=5)

## SECTION – C

Answer **any five** questions. Weightage **2** for **each**.

17. Distinguish between direct addressing mode and indirect addressing mode with the help of examples.
18. Write a short note about cache memory.
19. Explain register organization of CPU.
20. Explain briefly about the microprogrammed control unit.
21. Write a short note about associative memory.
22. Explain the characteristics of CISC systems.
23. Briefly explain about the direct memory access.
24. Explain about the different fixed point representations. (5×2=10)

## SECTION – D

Answer **any one** question. Weightage **4** for **each**.

25. What are the different types of instruction formats ? Explain.
  26. Explain in detail about asynchronous data transfer. (1×4=4)
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